

The diagram illustrates a computer system 100 enclosed in a dashed boundary. At the center is a box labeled **PROCESSOR** 106. Above the processor, a horizontal bus line 108 connects to three **Workstation** boxes 102. Below the processor, another horizontal bus line 110 connects to three **Workstation** boxes 104. A vertical line connects the processor 106 to both the upper bus 108 and the lower bus 110. To the left of the system, a vertical line 126 connects the processor area to two external device icons 122 and 124. An arrow 120 points towards the external devices.

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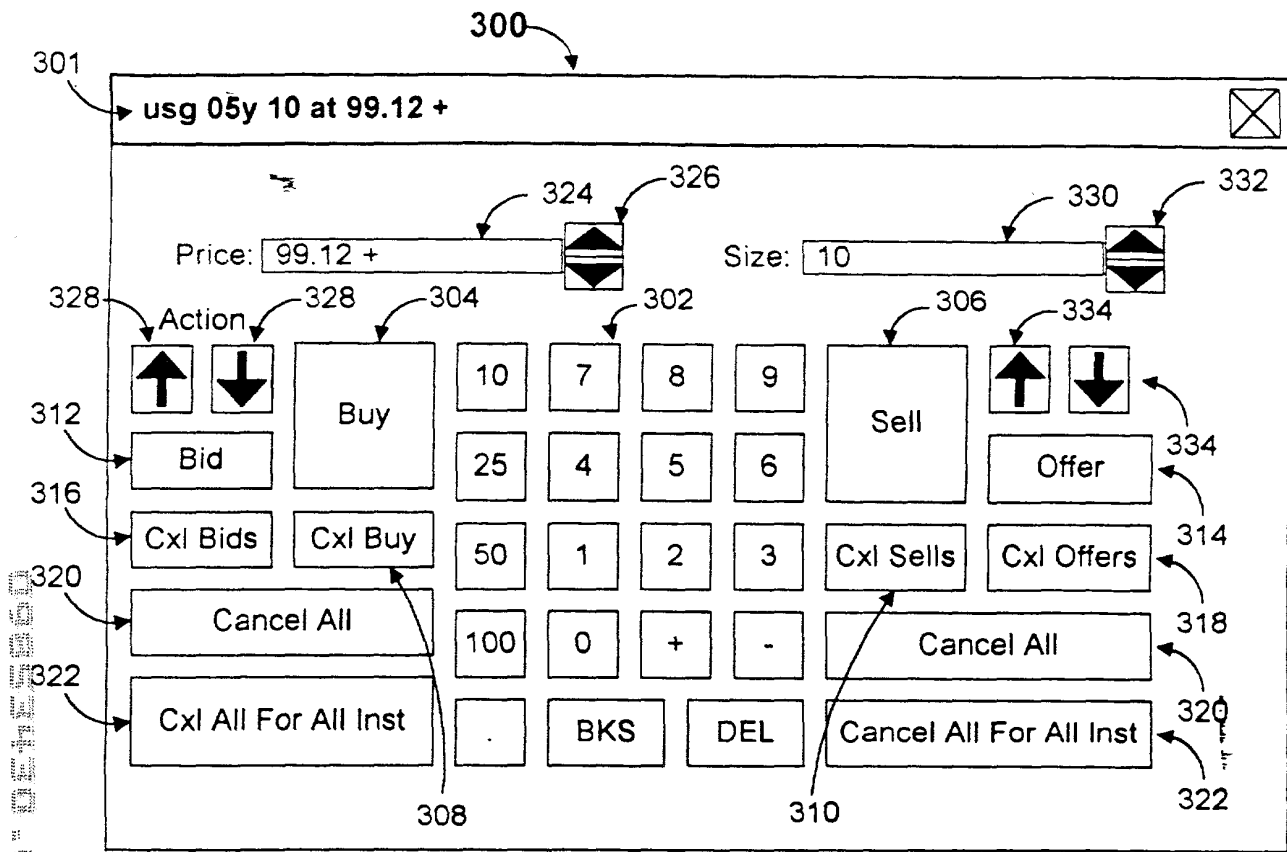
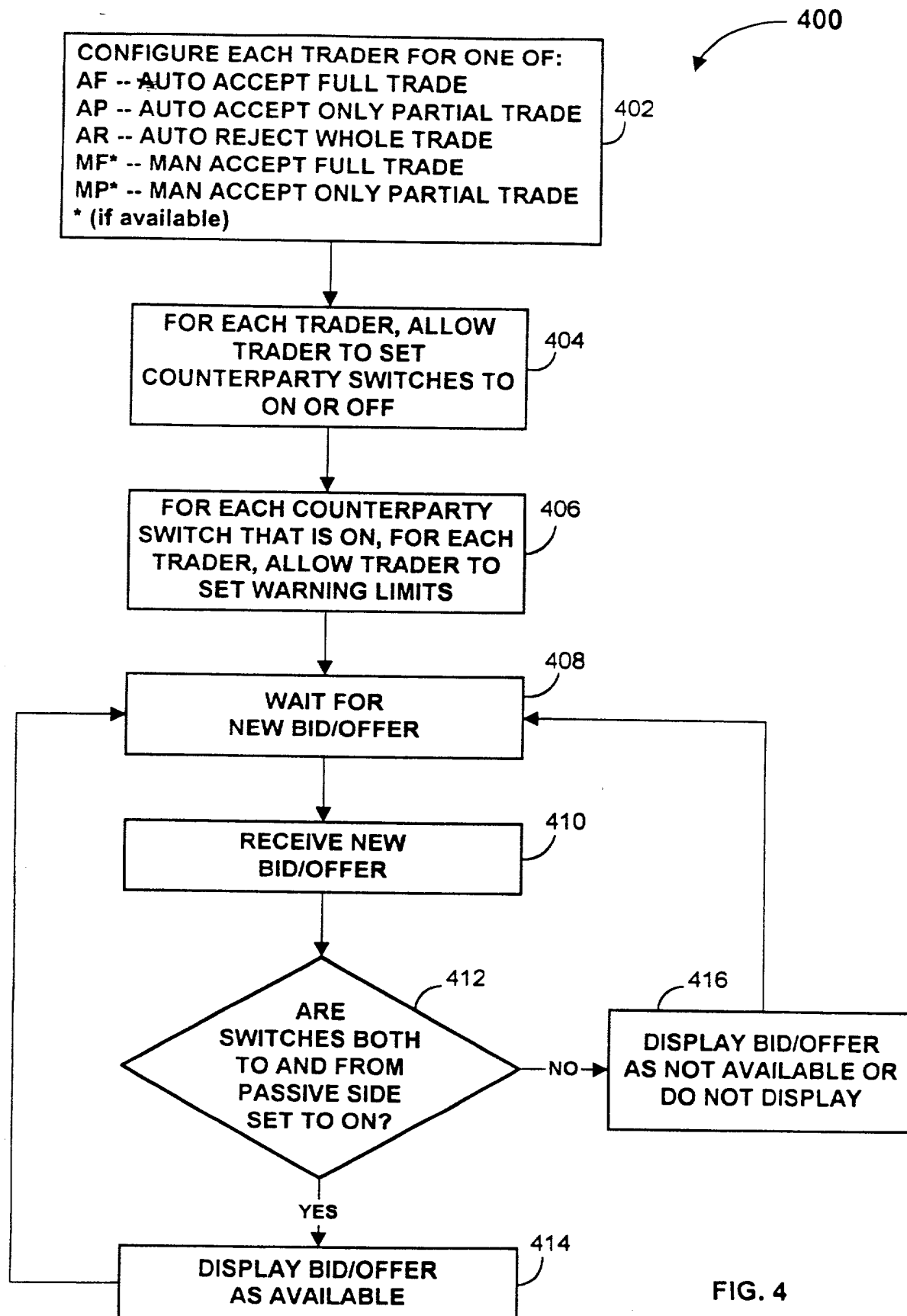
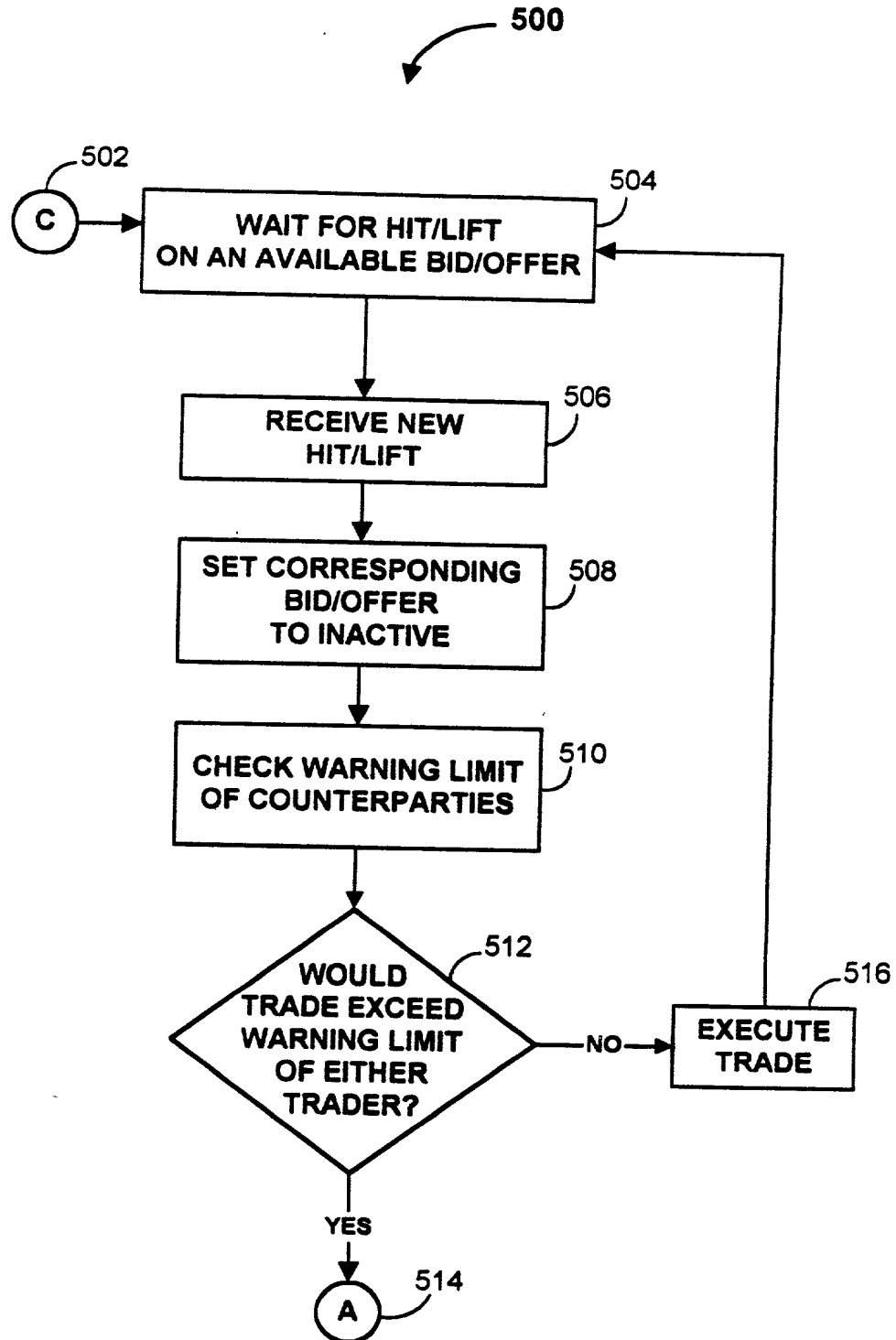


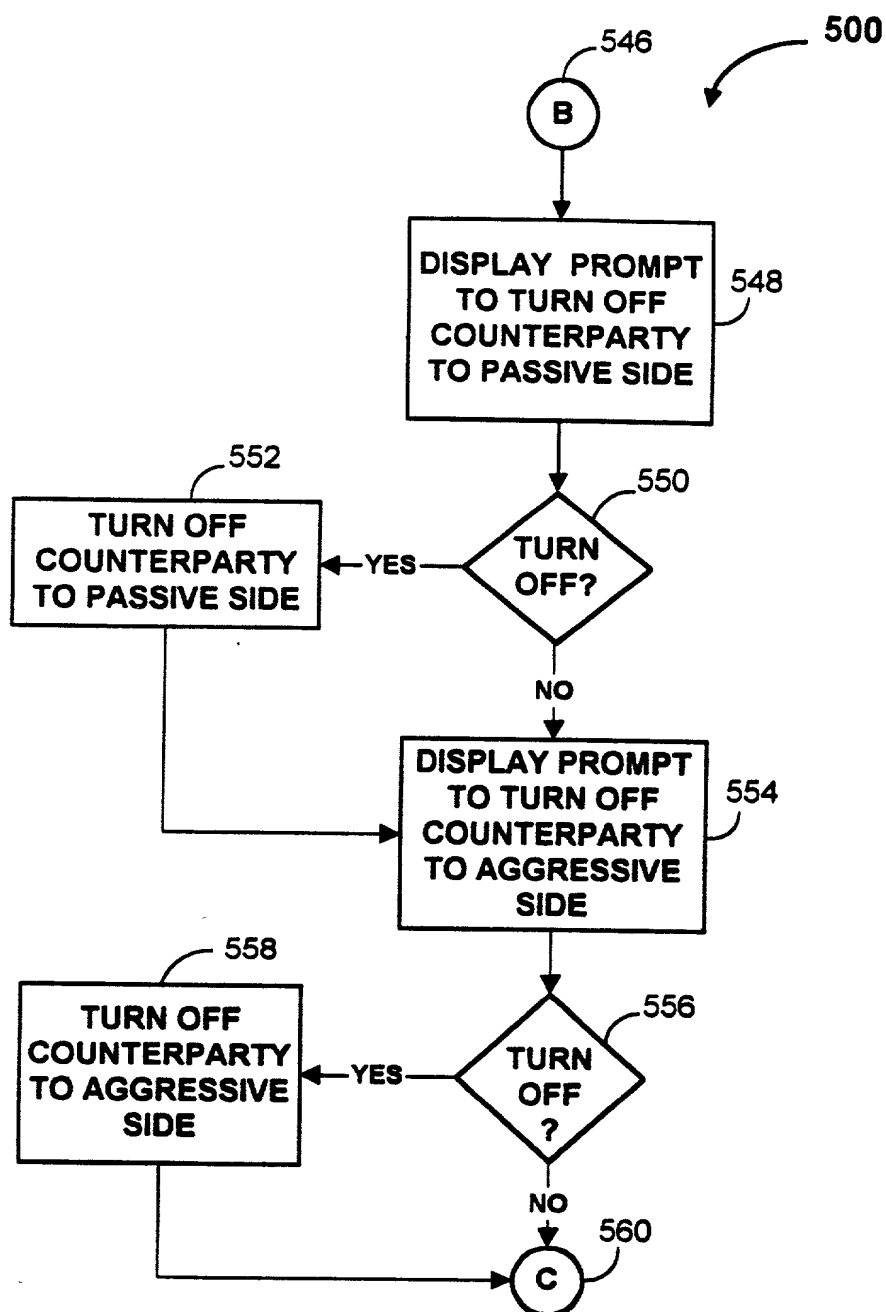
FIG. 3





**FIG. 5A**



[illegible]

**FIG. 5C**

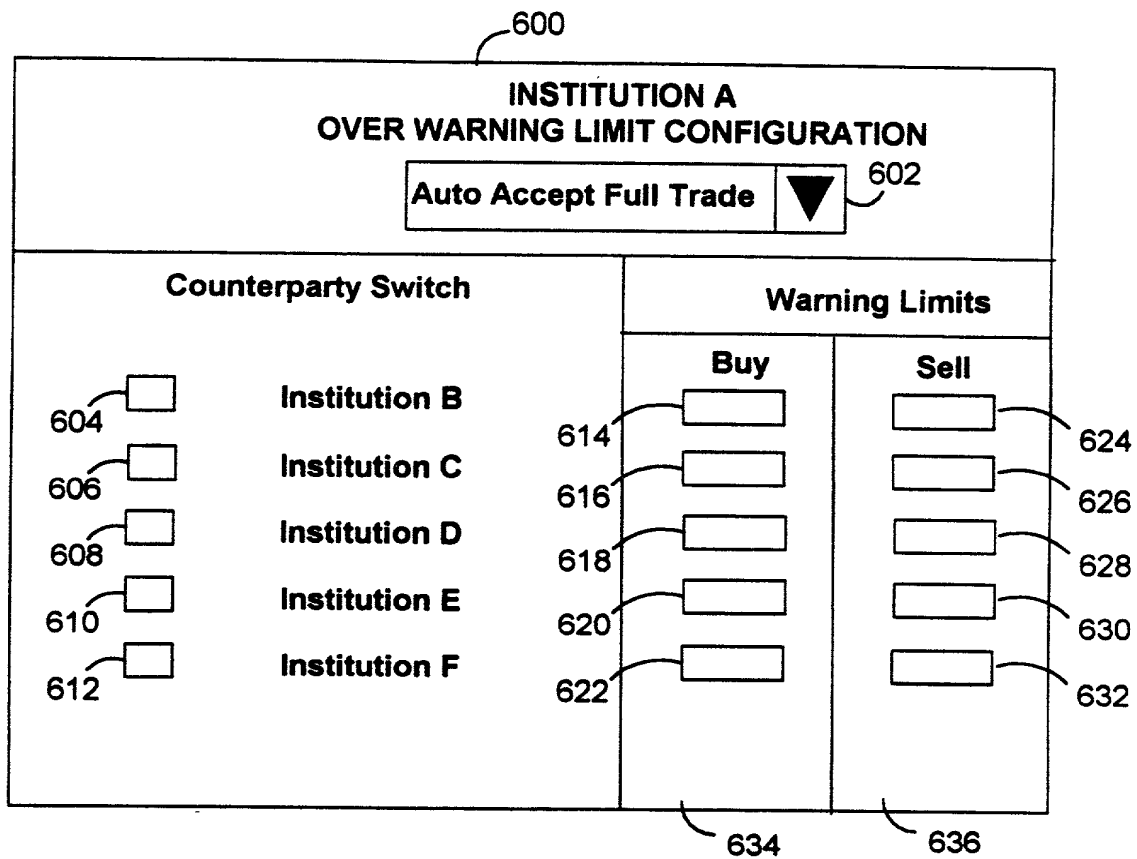
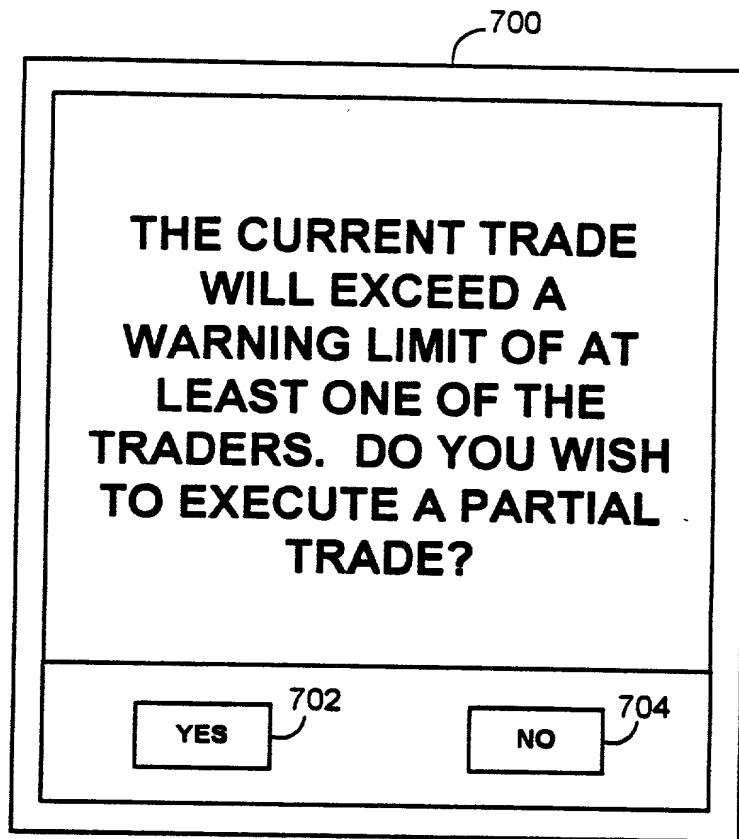
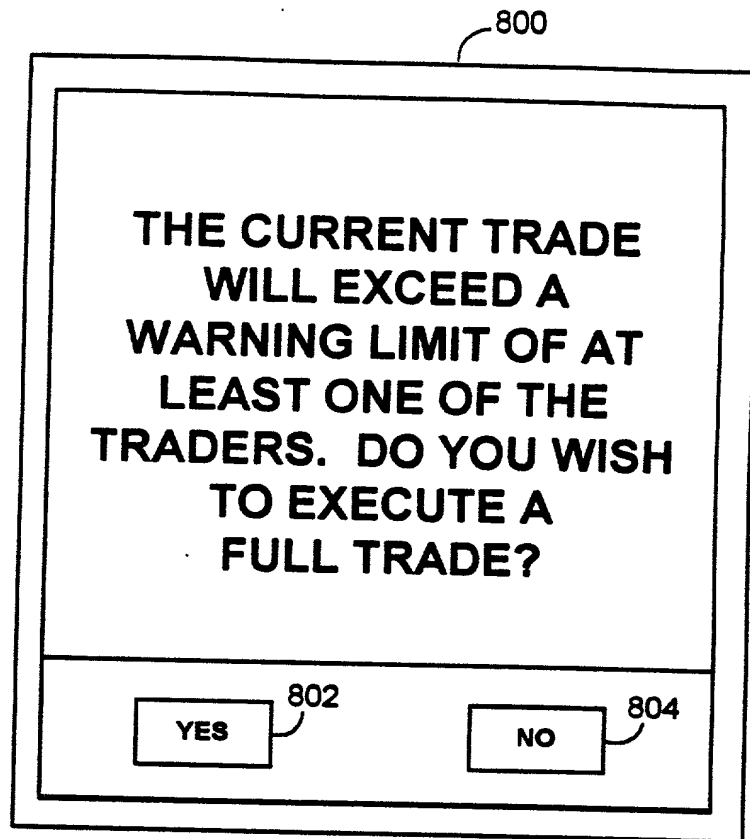


FIG. 6





**FIG. 7**



**FIG. 8**

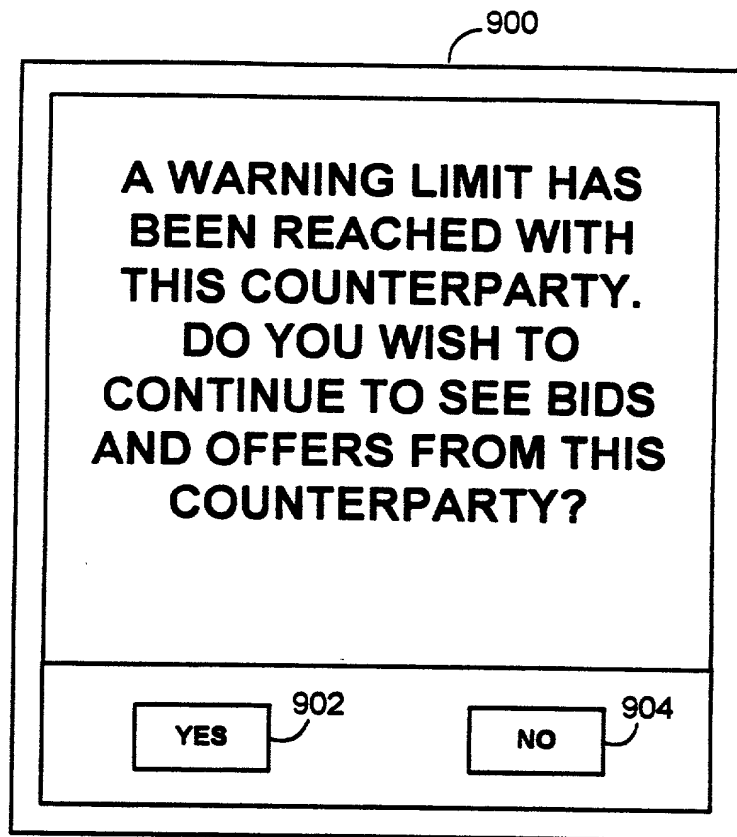


FIG. 9